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APPLICANTS

Kazutaka Shibata, Kyoto, JAPAN;

** CONTINUING DATA *Im2*
*none*** FOREIGN APPLICATIONS *Im*

JAPAN 2002-206168 07/15/2002

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35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance Verified and Acknowledged <i>Signature</i> Examiner's Signature <i>Im2</i> Initials				

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TITLE

Semiconductor device and manufacturing method thereof

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